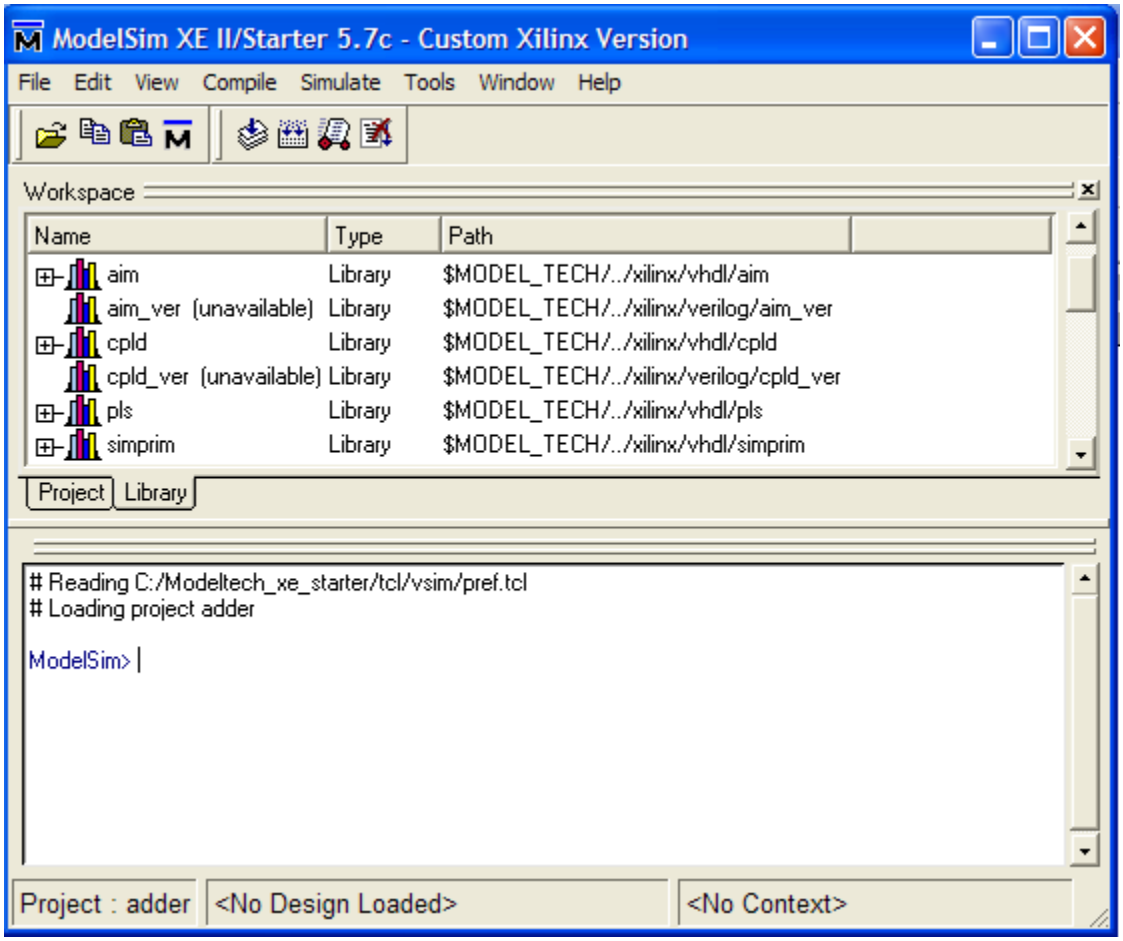
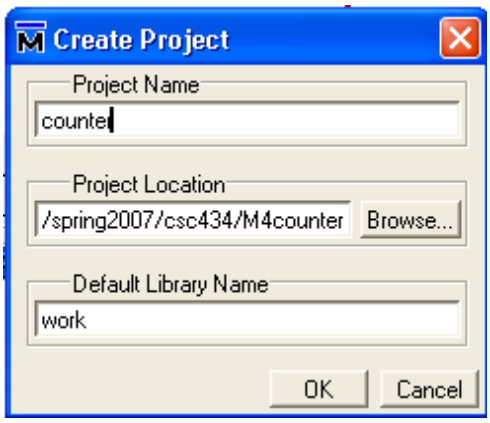
**Tutorial for students:**

For using modelsim, first, you have to have your VHDL files in some directory.

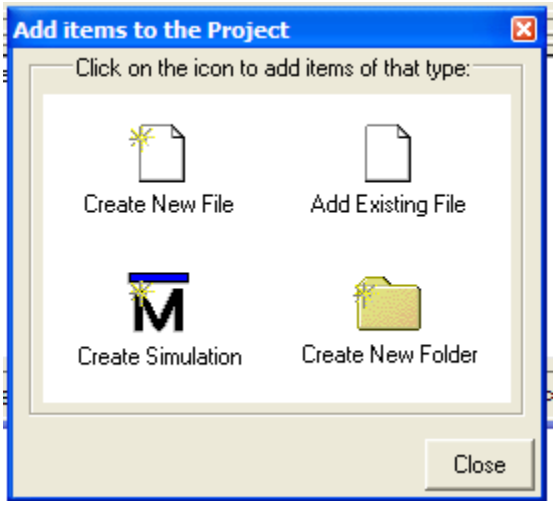
1. Click the icon of modelsim in Windows environment (or type VISM in Unix environment). It will give you a window as following:



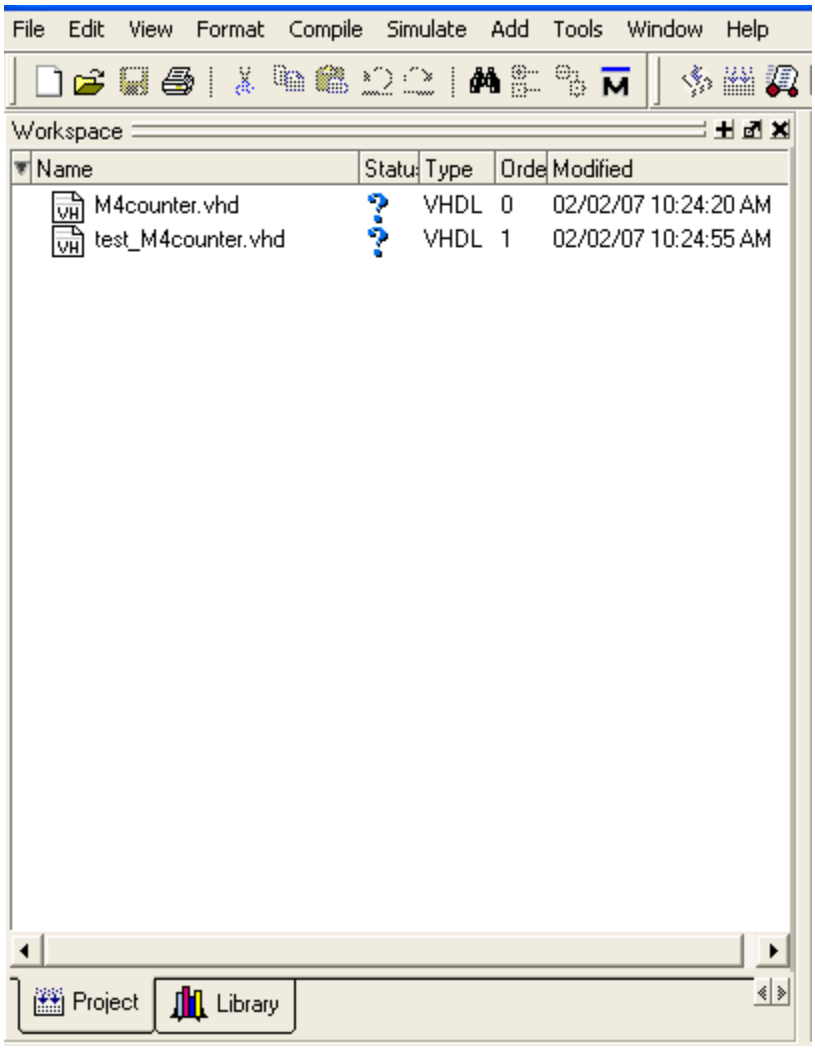
2. Click File → New→ Project →type in Project Name(counter)→Click Browse to select the location that you want(C:\minhua\spring2007\csc343\M4counter).



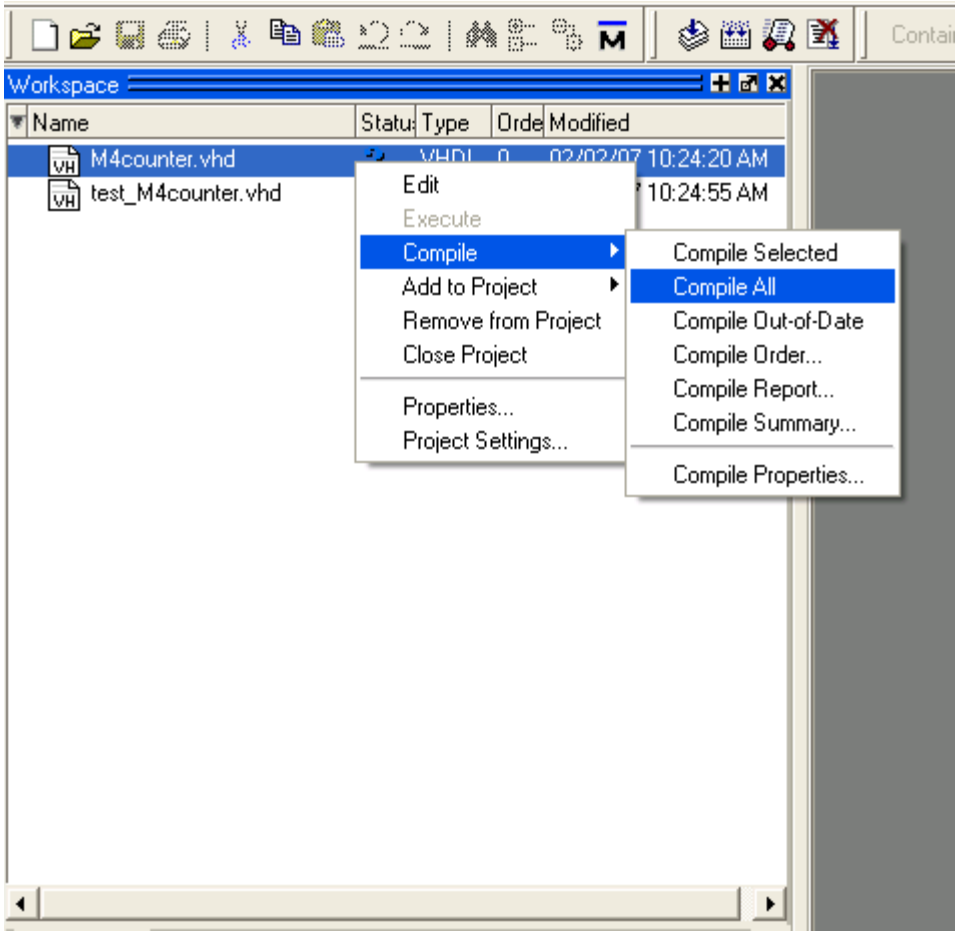

3. Click OK to get the window as following:



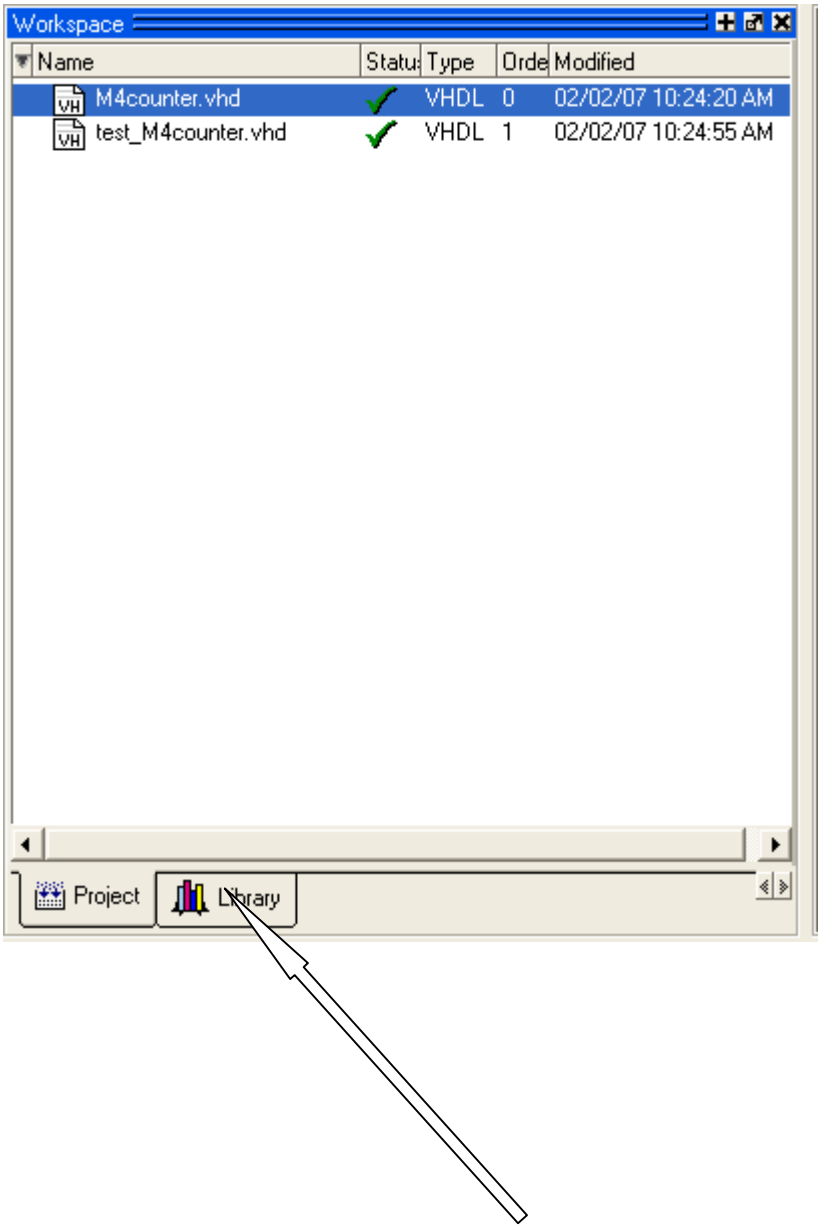
4. Select Add Existing File. (I select the existed VHDL files: M4counter.vhd and test_M4counter.vhd)



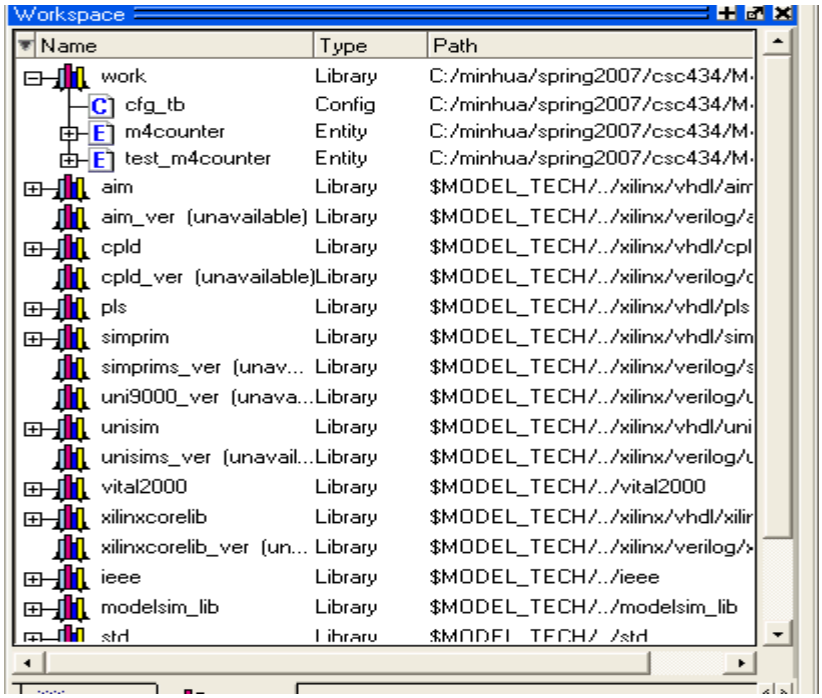
5. Compile these two files: high-lighten the M4counter.vhd →compile →compile all



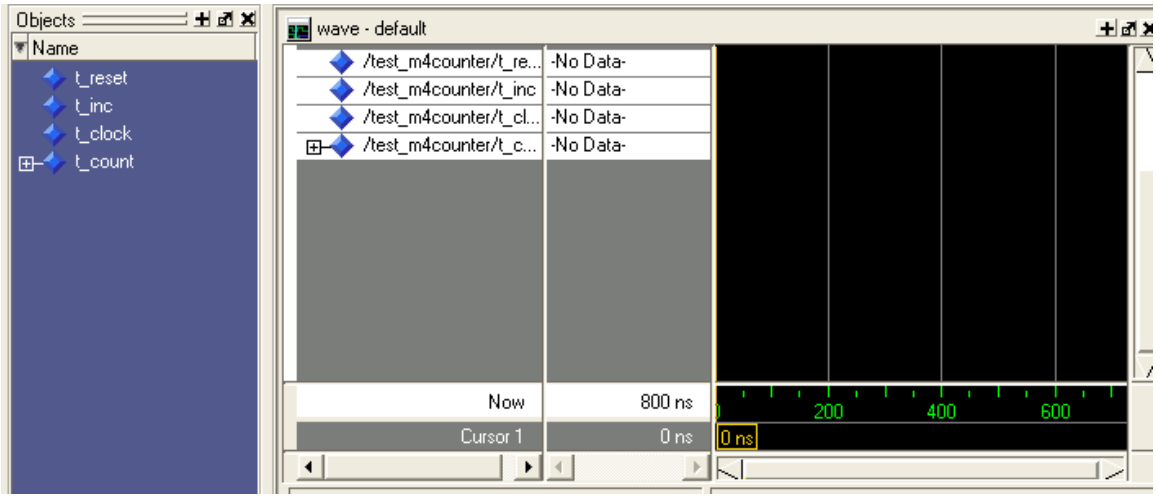
6. If your code has no syntax error, it will show a window as following:




7. In the window above, double click Library → click '+' next to work → double click test_M4counter → it will get a window as following:




8. click View → Signal → Wave , Click Edit (in the signals window) → Select All → drag all signals into wave window.



9. In the main window, click Simulation → Run 800ns→ Run All →click